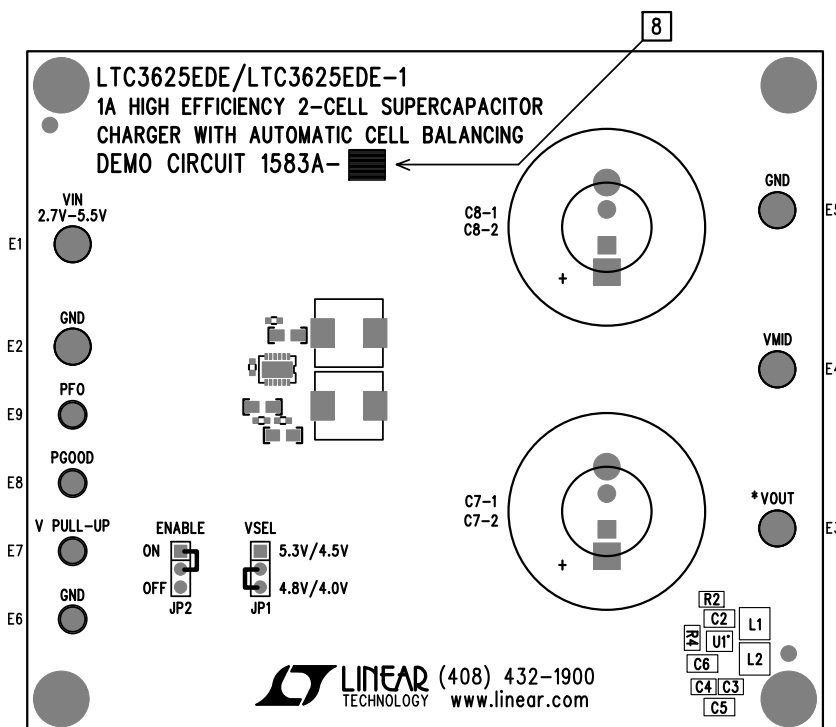


REVISION HISTORY				
ECO	REV	DESCRIPTION	APPR	DATE
-	2	PRODUCTION FAB	J. DREW	06-15-10

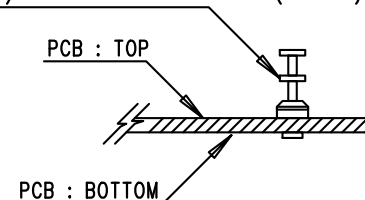


TOP SILKSCREEN
 LINEAR TECHNOLOGY
 DC1583A-2 * LTC3625EDE/LTC3625EDE-1
 1A HIGH EFFICIENCY 2-CELL SUPERCAPACITOR
 CHARGER WITH AUTOMATIC CELL BALANCING
 06-15-10

NOTES: UNLESS OTHERWISE SPECIFIED

1. WORKMANSHIP SHALL BE IN ACCORDANCE WITH IPC-A-610.
2. ASSEMBLY PROCESS SHALL INCLUDE: REFLOW SOLDER TOP SIDE SMD.
3. PARTS TO OMIT WILL BE SPECIFIED ON THE BILL OF MATERIALS
LOCATIONS OF OMITTED PARTS SHALL BE FREE OF SOLDER.
MASK THE SOLDER STENCIL WHERE SMT PARTS ARE OMITTED.
4. INSTALL SHUNTS AS SHOWN ON ASSY DRAWING.
5. DEPANELIZE BOARDS AFTER ASSEMBLY AND ROUTE-OUT THE BREAKOUT TABS ON FOUR SIDES OF THE BOARD EDGE.
6. MAXIMUM SOLDER TEMPERATURE IS 240 DEGREES CELCIUS.
7. INSTALL TURRETS AS SHOWN BELOW:

(5 PLCS) : MILL-MAX 2501-2 AND (3 PLCS) : MILL-MAX 2501-2



8. MARK EACH ASSEMBLY TYPE WITH BLACK PERMANENT MARKER AS SHOWN IN TABLE BELOW:

*

ASSY TYPE	U1	VOUT
-A	LTC3625EDE	5.3V-4.8V
-B	LTC3625EDE-1	4.5V-4.0V

APPROVALS

	INIT	DATE
PCB DES. NC		06-15-10
APP ENG. J. DREW		06-15-10

SCALE = NONE



1630 MCCARTHY BLVD
 MILPITAS, CA 95035
 PH: (408)432-1900
 www.Linear.com
 LTC CONFIDENTIAL-
 FOR CUSTOMER USE ONLY

TITLE: TOP ASSEMBLY DRAWING:
 1A HIGH EFFICIENCY 2-CELL SUPERCAPACITOR
 CHARGER WITH AUTOMATIC CELL BALANCING

SIZE N/A IC NO. LTC3625EDE/LTC3625EDE-1
 DEMO CIRCUIT 1583A-A/B

REV.
2

FILENAME: DC1583A-2.PCB

SHT 1 of 2